

## WEST Search History

DATE: Tuesday, August 24, 2004

Hide?	Set Name	Query	Hit Count
		<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i>	
<input type="checkbox"/>	L9	L7 and \$1MOS	61
<input type="checkbox"/>	L8	L7 and wiring board	2
<input type="checkbox"/>	L7	L6 and power supply	177
<input type="checkbox"/>	L6	bias voltage and junction temperature	367
<input type="checkbox"/>	L5	L3 and printed board	0
<input type="checkbox"/>	L4	L3 and wiring board	1
<input type="checkbox"/>	L3	L1 and (DUT or device under test)	93
<input type="checkbox"/>	L2	L1 and printed wiring board	29
<input type="checkbox"/>	L1	bias voltage and voltage supply	10634

END OF SEARCH HISTORY

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